

# A new algorithm for the propositional satisfiability problem

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## Abstract

A new enumeration algorithm is proposed for the propositional satisfiability problem. Such algorithm is based on a hypergraph formulation of the problem. Two different implementations of the algorithm are presented together with the results of an experimentation intended to compare their performance with the performance of other known methods. The computational results obtained are quite promising.

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## 1. Introduction

The satisfiability problem (SAT) is one of the most important combinatorial optimization problems. Since the pioneering work of Cook [5], who has shown it to be the first NP-complete problem, SAT is at the core of the computational complexity theory. Its importance is not only theoretical but also practical, because of its rôle in knowledge-based systems.

Here we present a new approach to its solution based on the use of directed hypergraphs. Directed hypergraphs, a generalization of digraphs, have been introduced in the last years in connection with different types of applications. A systematic presentation of hypergraph concepts and a large number of references are given in [11], where a hypergraph formulation of SAT is presented. From such formulation it follows that SAT can be solved through the solution of a (possibly exponentially large) sequence of easy path problems on hypergraphs. This idea is exploited in this paper in order to devise a new enumeration algorithm, which, from the experimentation performed so far, seems to be quite promising.

The plan of the paper is the following: in Section 2, the main hypergraph concepts are introduced and the hypergraph formulation of SAT is presented; in Section 3, our approach is described; the implementation issues are dealt with in Section 4; in

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Section 5 the experimentation performed is described and the results obtained are reported; finally, a few concluding remarks can be found in Section 6.

## 2. SAT and directed hypergraphs

Let  $\mathcal{X}$  be a set of  $n$  atomic propositions, which can be either *true* or *false*, and denote by  $T$  a proposition which is always *true*, and by  $F$  a proposition which is always *false*. Let  $\mathcal{C}$  be a set of  $m$  clauses, each of the form

$$x_1 \vee \dots \vee x_r \leftarrow x_{r+1} \wedge \dots \wedge x_q, \quad (1)$$

where  $x_i \in \mathcal{X}$ , for  $i = 1, \dots, q$ . The meaning of (1) is that at least one of the propositions  $x_1, \dots, x_r$  must be *true* when all the propositions  $x_{r+1}, \dots, x_q$  are *true*. If this is the case, the clause is *true*; otherwise, if  $x_1, \dots, x_r$  are all *false* and  $x_{r+1}, \dots, x_q$  are all *true*, then the clause is *false*. The disjunction  $x_1, \dots, x_r$  is called the *consequence* of the clause, while the conjunction  $x_{r+1}, \dots, x_q$  is called the *implicant*. We allow for  $r = 0$ , in which case the consequence is replaced by  $F$ , and for  $r = q$ , in which case the implicant is replaced by  $T$ .

A *truth assignment* is a function  $v: \mathcal{X} \rightarrow \{\text{false}, \text{true}\}$  that associates a value to each proposition. If a truth assignment which makes all the clauses *true* exists, then  $\mathcal{C}$  is said to be *satisfiable*, otherwise it is *unsatisfiable*.

The *satisfiability problem* (SAT) is then defined as follows:

### SAT.

*Input:* a set  $\mathcal{X}$  of  $n$  propositions, and a set  $\mathcal{C}$  of  $m$  clauses over  $\mathcal{X} \cup \{F, T\}$ .

*Output:* “yes” if  $\mathcal{C}$  is satisfiable; “no” otherwise.

Most often, if  $\mathcal{C}$  is a satisfiable set of clauses, one wants also a truth assignment which satisfies it. It is easy to see that (1) corresponds to the standard *disjunctive form*

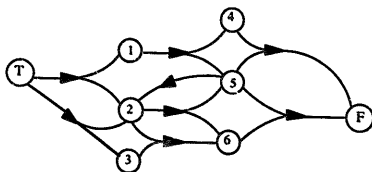
$$x_1 \vee \dots \vee x_r \vee \neg x_{r+1} \vee \dots \vee \neg x_q. \quad (2)$$

Hence, each instance  $\sigma = (\mathcal{X}, \mathcal{C})$  of SAT corresponds to a propositional formula,  $S$ , in *conjunctive normal form*, given by the conjunction of  $m$  disjunctions of type (2), one for each clause in  $\mathcal{C}$ .

### 2.1. Directed hypergraphs. Cuts and cutsets

We define a directed hypergraph (or simply *hypergraph*) as a pair  $\mathcal{H} = (\mathcal{V}, \mathcal{A})$  where  $\mathcal{V}$  is the set of nodes and  $\mathcal{A}$  is the set of *hyperarcs*. A hyperarc  $a$  is a pair  $(\text{Tail}(a), \text{Head}(a))$ , where  $\text{Tail}(a)$  and  $\text{Head}(a)$  are two nonempty disjoint subsets of  $\mathcal{V}$ . These hypergraphs are denominated *2-Graphs* in [11], where a more general and unifying definition of directed hypergraphs is proposed.

We define *size* of hyperarc  $a$  the number of nodes it contains, i.e.  $|a| = |\text{Tail}(a)| + |\text{Head}(a)|$ ; the size  $\mathcal{S}(\mathcal{H})$  of a hypergraph  $\mathcal{H}$  is the sum of the size of its hyperarcs.

Fig. 1. Hypergraph  $\mathcal{H}$ .

Let  $\mathcal{H} = (\mathcal{V}, \mathcal{A})$  be a hypergraph and  $s$  and  $t$  be two distinguished nodes. A *cut*  $\mathcal{F}_{st} = (\mathcal{V}_s^*, \mathcal{V}_t^*)$  is a partition of  $\mathcal{V}$  into two subsets  $\mathcal{V}_s^*$  and  $\mathcal{V}_t^*$  such that  $s \in \mathcal{V}_s^*$  and  $t \in \mathcal{V}_t^*$ .

Given the cut  $\mathcal{F}_{st}$ , its *cutset*  $\mathcal{E}_{st}$  is the set of all hyperarcs  $a$  such that  $\text{Tail}(a) \subseteq \mathcal{V}_s^*$  and  $\text{Head}(a) \subseteq \mathcal{V}_t^*$ . The *cardinality* of a cut is the cardinality of its cutset.

Given any instance  $\sigma = (\mathcal{X}, \mathcal{C})$  of SAT we can define the hypergraph  $\mathcal{H}_\sigma = (\mathcal{V}, \mathcal{A})$  associated with  $\sigma$ ;  $\mathcal{V}$  and  $\mathcal{A}$  are defined as follows:

- $\mathcal{V}$  contains a node for each proposition in  $\mathcal{X}$ , and two nodes corresponding to  $T$  and  $F$ ;
- $\mathcal{A}$  contains a hyperarc  $a_C$  for each clause  $C \in \mathcal{C}$ ; with  $\text{Tail}(a_C)$  and  $\text{Head}(a_C)$  containing the nodes corresponding to the implicant and the consequence of  $C$ , respectively.

As an example, the hypergraph in Fig. 1 corresponds to the following SAT instance:

$$C_1 = x_1 \vee x_2 \leftarrow T,$$

$$C_2 = x_2 \vee x_3 \leftarrow T,$$

$$C_3 = x_4 \vee x_5 \leftarrow x_1,$$

$$C_4 = x_5 \vee x_6 \leftarrow x_2,$$

$$C_5 = x_6 \leftarrow x_2 \wedge x_3,$$

$$C_6 = x_2 \leftarrow x_5,$$

$$C_7 = F \leftarrow x_4 \wedge x_5,$$

$$C_8 = F \leftarrow x_5 \wedge x_6.$$

It is possible to relate cuts in hypergraphs and truth assignments. Consider the hypergraph  $\mathcal{H}_\sigma = (\mathcal{V}, \mathcal{A})$  associated with an instance  $\sigma$  of SAT. Given a *TF-cut*  $\mathcal{F}_{TF} = (\mathcal{V}_T^*, \mathcal{V}_F^*)$  such that  $T \in \mathcal{V}_T^*$  and  $F \in \mathcal{V}_F^*$  it is possible to define the corresponding truth assignment as follows:

$$v(x) = \begin{cases} \text{true} & \text{if } x \in \mathcal{V}_T^*, \\ \text{false} & \text{if } x \in \mathcal{V}_F^*. \end{cases}$$

Conversely, given any truth assignment  $\nu$ , we can define a cut  $\mathcal{T}_{TF}$  as follows:

$$\mathcal{V}_T = \{x: \nu(x) = \text{true}\} \cup \{T\} \text{ and } \mathcal{V}_F = \{x: \nu(x) = \text{false}\} \cup \{F\}.$$

Suppose that there exists a hyperarc  $a_C$  in the cutset: it is easy to check that the corresponding clause  $C$  is made *false* by  $\nu$ . Hence, a truth assignment satisfying  $\sigma$  corresponds to a cut  $\mathcal{T}_{TF}$  with an empty cutset; the following theorem holds (see [11, Theorem 8]).

**Theorem 1.** *An instance  $\sigma \in \text{SAT}$  is satisfiable if and only if the associated hypergraph  $\mathcal{H}_\sigma$  has a cut  $\mathcal{T}_{TF}$  with cardinality 0.*

The satisfiability problems can be restated in terms of cuts in hypergraphs as follows:

#### SAT\_Empty\_cut.

*Input:* a hypergraph  $\mathcal{H}_\sigma$  associated with a SAT instance  $\sigma$ .

*Output:* “yes” if there exists in  $\mathcal{H}_\sigma$  a cut  $\mathcal{T}_{TF}$  with empty cutset; “no” otherwise.

Actually, many enumerative algorithms for SAT can be interpreted in terms of directed hypergraphs, as algorithms that find a cut with empty cutset, if one exists.

## 2.2. B-hypergraphs and Horn-SAT

It is well known that SAT is NP-complete; however, many classes of SAT instances are solvable in polynomial time (see, for example, [1, 2, 4, 12]). A widely known example of polynomially solvable SAT instances is the class of Horn formulas (Horn-SAT). The instances in this class contain *Horn clauses*, that is clauses with at most one variable (or the constant  $F$ ) in the consequence. For this class, linear-time algorithms are known (see [6]).

Hyperarcs corresponding to Horn clauses contain exactly one node in the head; these hyperarcs will be called *backward hyperarcs*, or simply *B-hyperarcs*. A hypergraph in which every hyperarc is a B-hyperarc is called *backward hypergraph*, or *B-hypergraph*; hence the hypergraph  $\mathcal{H}_\sigma$  associated with a Horn instance  $\sigma$  is a B-hypergraph. The satisfiability problem for Horn formulas can be easily treated exploiting some of the properties of B-hypergraphs. The reader is referred to [11] for a wider study of B-hypergraphs; in the following we will recall some basic definitions, related to the application to Horn-SAT.

A path  $P_{st}$  of length  $q$ , in the B-hypergraph  $\mathcal{H} = (\mathcal{V}, \mathcal{A})$  is a sequence

$$P_{st} = (n_1 = s, a_1, n_2, a_2, \dots, a_q, n_{q+1} = t),$$

where  $n_1, n_2, \dots, n_{q+1}$  are nodes of  $\mathcal{V}$ ;  $a_1, a_2, \dots, a_q$  are B-hyperarcs of  $\mathcal{A}$ ; and  $n_j \in \text{Tail}(a_j)$ ,  $\text{Head}(a_j) = \{n_{j+1}\}$ ,  $j = 1, \dots, q$ .

Nodes  $s$  and  $t$  are the *origin* and the *destination* of  $P_{st}$ , respectively; if  $s = t$  then the path  $P_{st}$  is a *cycle*. We say that node  $y$  is *connected* to a node  $x$  in a B-hypergraph  $\mathcal{H}$  if a path  $P_{xy}$  exists in  $\mathcal{H}$ .

A *B-hyperpath*  $\Pi_{st}$  in the B-hypergraph  $\mathcal{H} = (\mathcal{N}, \mathcal{A})$  is a minimal cycle-free B-hypergraph  $\mathcal{H}_\Pi = (\mathcal{N}_\Pi, \mathcal{A}_\Pi)$  such that

- (i)  $\mathcal{A}_\Pi \subseteq \mathcal{A}$ ;
- (ii)  $s, t \in \mathcal{N}_\Pi = \bigcup_{a_j \in \mathcal{A}_\Pi} \text{Tail}(a_j) \cup \text{Head}(a_j) \subseteq \mathcal{N}$ ;
- (iii)  $x \in \mathcal{N}_\Pi \Rightarrow x$  is connected to  $s$  in  $\mathcal{H}_\Pi$ .

Node  $t$  is *B-connected* to node  $s$  in  $\mathcal{H}$  if a B-hyperpath  $\Pi_{st}$  exists in  $\mathcal{H}$ . The following proposition trivially holds.

**Proposition 1.** *Given a B-hyperpath  $\Pi_{st}$  and a B-hyperarc  $a_j \in \mathcal{A}_\Pi$ , one has that each node  $x \in \text{Tail}(a_j)$  is B-connected to  $s$  in  $\Pi_{st}$ .*

A *B-hyperpath*  $\Pi$  from the set of nodes  $B \subseteq \mathcal{N}$  to the node  $t$  in  $\mathcal{H} = (\mathcal{N}, \mathcal{A})$  is a minimal cycle-free B-hypergraph satisfying condition (i) and the following conditions:

- (iv)  $B \cup \{t\} \subseteq \mathcal{N}_\Pi = \bigcup_{a_j \in \mathcal{A}_\Pi} \text{Tail}(a_j) \cup \text{Head}(a_j) \subseteq \mathcal{N}$ ;
- (v)  $x \in \mathcal{N}_\Pi \Rightarrow$  there exists  $s \in B$  such that  $x$  is connected to  $s$  in  $\mathcal{H}_\Pi$ .

It is possible to relate the concepts of cut and B-connection in B-hypergraphs; in fact, the following theorem holds (Theorem 1 in [11]).

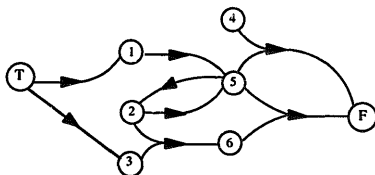
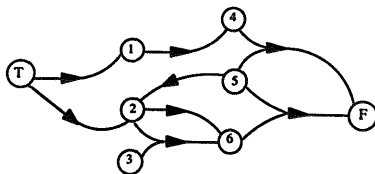
**Theorem 2.** *In a B-hypergraph  $\mathcal{H}$  a cut  $\mathcal{F}_{st}$  of cardinality 0 exists if and only if  $t$  is not B-connected to  $s$ .*

The above theorem can be seen as an extension to B-hypergraphs of a well-known dual relation holding for “standard” directed graphs, where there exists either a path from node  $s$  to node  $t$  or a cut with empty cutset separating  $s$  and  $t$ .

A relevant consequence of Theorem 2 is that a Horn instance  $\sigma$  of SAT is satisfiable if and only if in the associated B-hypergraph  $\mathcal{H}_\sigma$   $F$  is not B-connected to  $T$ . Hence, Horn-SAT can be solved searching a B-hyperpath from  $T$  to  $F$  in  $\mathcal{H}_\sigma$ ; if such a B-hyperpath is not found, then  $\sigma$  is satisfiable.

The problem of finding a B-hyperpath from  $T$  to  $F$  can be easily solved by a suitable visit of  $\mathcal{H}_\sigma$ . The procedure *B-Visit* (see [11]) finds the set of nodes that are B-connected to a given node  $r$  in a B-hypergraph; moreover, it returns a set of B-paths containing all the nodes B-connected to  $r$ . The time complexity of *B-Visit* is  $O(\mathcal{S}(\mathcal{H}))$ ; it follows that *B-Visit* solves an instance of Horn-SAT in linear time.

Actually, *B-Visit*, which bears a strong resemblance with the linear algorithm for Horn-SAT proposed by Dowling and Gallier [6], can be seen as a clever implementation of *positive unit resolution* (i.e., resolution where one of the resolving clauses is restricted to be a *positive unit clause*  $x \leftarrow T$ ; see [17]). When a set  $\mathcal{G}$  of Horn clauses is satisfiable, the algorithm above finds the set  $\mathcal{V}_T$  of propositions that are implied by  $\mathcal{G}$ , i.e. propositions  $x$  such that  $v(x) = \text{true}$  in each truth assignment  $v$  satisfying  $\mathcal{G}$ . Once  $\mathcal{V}_T$  has been found, an assignment satisfying  $\mathcal{G}$  can be obtained simply setting  $v(x) = \text{false}$  for each  $x \notin \mathcal{V}_T$ . When the formula is not satisfiable, the algorithm finds

Fig. 2. B-reduction  $\mathcal{H}_B^1$  of hypergraph  $\mathcal{H}$ .Fig. 3. B-reduction  $\mathcal{H}_B^2$  of hypergraph  $\mathcal{H}$ .

a refutation, i.e. a proof that  $\mathcal{C}$  implies  $v(F) = \text{true}$ . In fact, it can be proved that nodes B-connected to  $T$  in  $\mathcal{H}_B$  correspond to propositions in the set  $\mathcal{V}_T$ ; moreover, a B-hyperpath from  $T$  to  $F$  corresponds to a refutation of  $\mathcal{C}$ .

### 2.3. B-reductions

In this section we will investigate the relations between B-connection in B-hypergraphs and cuts in general hypergraphs, introducing the concept of B-reduction.

Given a hyperarc  $a = (\text{Tail}(a), \text{Head}(a))$ , a B-reduction of  $a$  is a B-hyperarc  $a_B = (\text{Tail}(a_B), \{v\})$  such that  $\text{Tail}(a_B) = \text{Tail}(a)$  and  $v \in \text{Head}(a)$ . A B-reduction of a hypergraph  $\mathcal{H}$  is the B-hypergraph  $\mathcal{H}_B$  obtained from  $\mathcal{H}$  by replacing each hyperarc by one of its B-reductions. As an example, consider the hypergraph  $\mathcal{H}$  in Fig. 1; two possible B-reductions of  $\mathcal{H}$ ,  $\mathcal{H}_B^1$  and  $\mathcal{H}_B^2$ , are shown in Figs. 2 and 3, respectively. The hyperarcs corresponding to the non-Horn clauses  $\{C_1, C_2, C_3, C_4\}$  have been replaced by one of their B-reduction in order to obtain a B-hypergraph.

Clearly, a hypergraph may have many B-reductions; we shall denote by  $\mathcal{B}(\mathcal{H})$  the set of all the B-reductions of  $\mathcal{H}$ . We say that node  $t$  is *super-connected* to node  $s$  in hypergraph  $\mathcal{H}$  if  $t$  is B-connected to  $s$  in any B-reduction  $\mathcal{H}_B$  of  $\mathcal{H}$ . Then,  $t$  is not super-connected to  $s$  if there exists at least one B-reduction in which  $t$  is not

B-connected to  $s$ . The following theorem (see [11]) relates super-connection and cuts in hypergraphs.

**Theorem 3.** *In a hypergraph  $\mathcal{H} = (\mathcal{V}, \mathcal{A})$  a cut  $\mathcal{F}_s$  of cardinality 0 exists if and only if  $t$  is not super-connected to  $s$ .*

The meaning of this theorem in the context of satisfiability is that a SAT instance  $\sigma$  is satisfiable if and only if in the associated hypergraph  $\mathcal{H}_\sigma = (\mathcal{V}, \mathcal{A})$ , node  $F$  is not super-connected to node  $T$ , i.e. there exists a B-reduction  $\mathcal{H}_B$  of  $\mathcal{H}_\sigma$  such that  $F$  is not B-connected to  $T$  in  $\mathcal{H}_B$ .

It is easy to see that any given B-reduction  $\mathcal{H}_B$  of  $\mathcal{H}_\sigma$  can be interpreted as the hypergraph associated with a *Horn subformula*  $S_B$  of a formula  $S$ ; here  $S_B$  is obtained by deleting all but one of the propositions from the consequence of every clause in  $S$ . Hence, Theorem 3 means that a given formula  $S$  is satisfiable if and only if there exists a satisfiable Horn subformula  $S_B$  of  $S$ .

In fact, it is easy to see that a truth assignment satisfying  $S_B$  also satisfies  $S$ ; moreover, given a truth assignment  $v$  satisfying  $S$ , we can easily find a Horn subformula  $S_B$  satisfied by  $v$ , provided that in the B-reduction,  $a_B = (\text{Tail}(a), \{n\})$ , of a hyperarc  $a$  we choose a node  $n \in \text{Head}(a)$  such that  $v(n) = \text{true}$ , if one exists (if such a node does not exist, any choice of  $n$  will fit).

Theorem 3 suggests a new algorithmic approach for SAT. In fact, a satisfiability problem can be solved by enumerating B-reductions of the associated hypergraph, until a satisfiable one (i.e. one in which  $F$  is not B-connected to  $T$ ) is found, if any. Note that once a B-hyperpath  $\Pi$  is found in a B-reduction that is not satisfiable, then the generation of B-reductions containing  $\Pi$  should be avoided; this fact can provide a guidance for the enumeration.

It is worth noting that in this approach the satisfiability problem reduces to a sequence of easily solvable Horn-SAT problems. A previous attempt in this direction was made by Gallo and Urbani [13]: in their algorithm a *Horn relaxation* of the problem is solved in each node of an enumeration tree. However, the proposed approach is quite different, since here the Horn instances that are solved are not relaxations.

It must be observed that the enumeration of B-reductions could be very inefficient in practice. In fact, the number  $|\mathcal{B}(\mathcal{H})|$  of possible B-reductions grows exponentially in the size  $|\mathcal{S}(\mathcal{H})|$  of the hypergraph  $\mathcal{H}_\sigma$ ; moreover, the size  $|\mathcal{S}(\mathcal{H})|$  itself can be exponentially large in the number of variables  $n$ . Hence, the number  $O(|\mathcal{B}(\mathcal{H})|)$  of B-reductions that must be (implicitly) enumerated could be exponentially larger than the number  $2^n$  of possible truth assignments.

### 3. A new implicit enumeration algorithm based on B-reductions

Here we present a new implicit enumeration algorithm for SAT, which is based on the use of B-reductions. This algorithm can be considered an implementation of the idea of enumerating the B-reductions discussed in the previous section.

Like all implicit enumeration methods, the proposed algorithm generates a search tree where the root corresponds to the original problem to be solved, and each node corresponds to a subproblem generated from the problem corresponding to the parent node by fixing the value of one or more variables. At each step, the leaf nodes correspond to either subproblems whose solution has been already found (i.e. which have been proved either to be satisfiable or to be unsatisfiable) or to subproblems not yet solved and whose descendants are still to be generated.

The algorithm stops when either a subproblem is recognized to be satisfiable or all the leaf nodes of the current search tree correspond to subproblems which have been recognized to be unsatisfiable. In the former case the answer is “yes” while in the latter the answer is “no”.

The nodes corresponding to subproblems that have not yet been solved and whose descendants are still to be generated are called *active nodes* and are maintained in a priority queue  $Q$ ; when the algorithm starts,  $Q$  contains only the original problem. At each iteration a node is removed from  $Q$  and the corresponding subproblem  $P$  is examined. In the scanning of  $Q$  we use a *depth-first* strategy, i.e. the last node inserted is the one to be removed first.

Each subproblem  $P$  has an associated hypergraph  $\mathcal{H}_P$  which is obtained from the hypergraph  $\mathcal{H}_\sigma$  associated with the original instance  $\sigma$  as follows. Let  $\mathcal{A}^T$ ,  $\mathcal{A}^F$ , and  $\mathcal{A}^*$  be the sets of nodes corresponding to propositions whose value is fixed to *true*, propositions whose value is fixed to *false*, and propositions whose value has not yet been fixed, respectively. We delete from  $\mathcal{H}_\sigma$  all the hyperarcs  $a$  corresponding to already satisfied clauses (i.e.  $\text{Head}(a) \cap \mathcal{A}^T \neq \emptyset$  or  $\text{Tail}(a) \cap \mathcal{A}^F \neq \emptyset$ ), and all the nodes in  $\mathcal{A}^T$  ( $\mathcal{A}^F$ ) from the tails (heads) of the remaining hyperarcs; when a tail (head) becomes empty it is replaced by  $T$  ( $F$ ).

A subproblem  $P$  is examined in two steps which will be described in detail in Section 4. First we apply a unit resolution algorithm: this step can be considered as a kind of *relaxation operation* and is similar to the basic step of the Horn relaxation algorithm proposed by Gallo and Urbani, where unit resolution is implemented as a forward visit of the hypergraph from the node  $T$ ; here, the main difference is that the visit is performed also from  $F$ , in a backward fashion. Note that the unit resolution is also used as a tool to fix the value of some variables; hence, in this step the hypergraph  $\mathcal{H}_P$  associated with  $P$  can be further simplified by deleting nodes and hyperarcs as described above. The second step can be interpreted as a *restriction operation* and consists in the generation of a B-reduction  $\mathcal{H}_B$  of  $\mathcal{H}_P$ , and in the search for a B-hyperpath from  $T$  to  $F$  in  $\mathcal{H}_B$ . If no such B-hyperpath exists then a  $TF$ -cut with empty cutset is found in  $\mathcal{H}_P$ , and  $P$  is proved to be satisfiable. Otherwise, let  $\Pi = (\mathcal{V}_\Pi, \mathcal{A}_\Pi)$  a B-hyperpath from  $T$  to  $F$  in  $\mathcal{H}_P$ , and let  $\{n_1 \dots n_k\}$  be the set of nodes in  $\mathcal{V}_\Pi$  different from  $T$  and  $F$ . It is easy to see that the variables  $n_1 \dots n_k$  cannot be all *true* in any truth assignment satisfying  $P$ . The set of variables  $\{n_1 \dots n_k\}$  is called the *branching set*, and the algorithm generates  $k$  new subproblems  $P_1, P_2, \dots, P_k$  by fixing the variables according to the following rule:



$P_1: n_1 = \text{false};$   
 $P_2: n_1 = \text{true}; n_2 = \text{false};$   
 $P_3: n_1 = n_2 = \text{true}; n_3 = \text{false};$   
 $\vdots$   
 $P_k: n_1 = \dots = n_{k-1} = \text{true}; n_k = \text{false};$

Subproblems  $P_1, \dots, P_k$  replace  $P$  in  $Q$ . The multiple branching rule used is the one which is widely used in TSP to eliminate subtours (see, for example, [3]). Such rule has also been used in enumerative algorithms for SAT by Monien and Speckenmeyer [19] and by Gallo and Urbani [13].

We can state more formally the resulting algorithm as follows.

#### Algorithm B-Enum

**Step 0:** Let  $P_0$  be the problem corresponding to the input instance  $\sigma$ ; set  $Q = \{P_0\}$ ;  
**Step 1:** If  $Q$  is empty, return “no”; otherwise remove a subproblem  $P$  from  $Q$ ;  
**Step 2:** Apply unit resolution to  $P$ ; if a contradiction is found, go to Step 1;  
 if  $P$  is solved, return “yes”;  
**Step 3:** Select a B-reduction  $\mathcal{H}_B$  of the hypergraph  $\mathcal{H}_P$  corresponding to  $P$ ;  
 if node  $F$  is not connected to node  $T$  in  $\mathcal{H}_B$  then return “yes”;  
 otherwise let  $\Pi \subseteq \mathcal{H}_B$  be a B-hyperpath from  $T$  to  $F$ ;  
**Step 4:** Let  $\{n_1 \dots n_k\}$  be the nodes in  $\Pi$  different from  $T$  and  $F$ ;  
 Add to  $Q$  the subproblems  $P_1, P_2, \dots, P_k$  generated from  $P$  as follows:  
 in problem  $P_i$ :  $n_i$  is set to *false*, and  $n_j, 1 \leq j < i$ , are set to *true*.  
 Goto Step 1.

Note that it is not necessary to use the whole set  $\{n_1 \dots n_k\}$  in the branching step. In fact any set  $B'$  such that there exists in  $\mathcal{H}$  a subhyperpath of  $\Pi$  from  $B'$  to  $F$  can be used as the branching set. A B-hyperpath  $\pi = (\mathcal{N}_\pi, \mathcal{A}_\pi)$  is a subhyperpath of  $\Pi$  if  $\mathcal{A}_\pi \subseteq \mathcal{A}_\Pi$  and  $\mathcal{N}_\pi \subseteq \mathcal{N}_\Pi$ . As an example, consider the B-reduction  $\mathcal{H}_B^1$  in Fig. 2; a B-hyperpath  $\Pi$  from  $T$  to  $F$  contained in  $\mathcal{H}_B^1$  is shown in Fig. 4 (nodes and hyperarcs in  $\Pi$  are shown in thick lines). The set of nodes  $B = \{n_3, n_5\}$  gives a valid branching set. In fact, if the corresponding variables are set to *true* in the subproblem corresponding to the hypergraph  $\mathcal{H}$  of Fig. 1, a contradiction arises.

A particular branching set is the set of propositions that correspond to the tail of the hyperarc incident with node  $F$  in the B-hyperpath  $\Pi$ . Such set is the one which has been used in the implementation of our algorithm.

#### 4. Implementation

In this section the implementation of Step 2 (unit resolution) and Step 3 of the algorithm are described in detail. The resulting procedures use data structures representing directed hypergraphs, that are an extension of those for directed graphs. A hypergraph is represented as a set of lists. For each node  $n$  there are two lists, one pointing to the hyperarcs in its *forward star*  $FS(n) = \{a \in \mathcal{A} : n \in \text{Tail}(a)\}$  and one

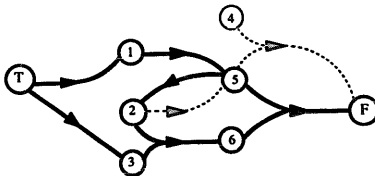


Fig. 4. B-hyperpath  $\Pi$  from  $T$  to  $F$  in  $\mathcal{H}_B^1$ .

pointing to the hyperarcs in its *backward star*  $BS(n) = \{a \in \mathcal{A} : n \in \text{Head}(a)\}$ . Similarly, for each hyperarc  $a$  there are two lists, one pointing to the nodes in  $\text{Tail}(a)$  and one pointing to the nodes in  $\text{Head}(a)$ .

#### 4.1. Unit resolution

As mentioned above, the procedure B-visit can be seen as an efficient implementation of positive unit resolution on Horn instances; it is easy to see that a slight modification of B-visit can perform positive unit resolution on general hypergraphs. Moreover, the resulting procedure can be extended in order to implement *complete* unit resolution: to do this, a *backward* visit of the hypergraph starting from the node  $F$  is executed together with the usual forward visit from  $T$ .

In practice, a variable  $n$  is set to *true* or *false* when an hyperarc  $(\{T\}, \{n\})$  or, respectively,  $(\{n\}, \{F\})$  is found; the corresponding nodes are deleted from the hypergraph during the visit, together with hyperarcs corresponding to satisfied clauses. Note that deletions are performed *logically*: nodes that are assigned a value are implicitly deleted, while a hyperarc  $a$  is deleted by setting a flag  $D[a]$  to *true*. For each hyperarc  $a$  the counters  $T[a]$  and  $H[a]$  are used to keep track of deleted nodes in  $\text{Tail}(a)$  and  $\text{Head}(a)$ , in the same way as it is done in B-visit.

The current truth assignment is kept in a vector  $V$ ; for each node  $n$  we have  $V[n] \in \{\text{true}, \text{false}, \text{undefined}\}$ . Two sets  $Q^T$  and  $Q^F$  are used to store the nodes to be fixed to *true* and *false*, respectively. The procedure returns the value “inconsistent” if a contradiction is detected. A formal description of the procedure is given below.

It is easy to see that procedure Unit-Resolution has the same complexity of B-visit, i.e.  $O(\mathcal{S}(\mathcal{H}))$ . So, Step 2 is implemented in linear time, thus extending to general instances the linear-time implementation of the complete unit resolution algorithm proposed by Minoux for Horn formulas [18].

It is possible to prove that a slight modification of procedure Unit-Resolution has an  $O(\mathcal{S}(\mathcal{H}))$  *branch complexity*. By branch complexity we mean the worst-case bound for the time spent by the procedure in the examination of all the subproblems belonging to a single *branch*, that is to a path from the root to a leaf in the enumeration tree. A linear branch complexity can be obtained restarting the visit of the hypergraph from the situation left after solving the predecessor of the current subproblem.

**Procedure Unit\_Resolution( $\mathcal{H}$ : hypergraph)**

```

begin
  for each  $n \in \mathcal{N}$  do  $V[n] := \text{undefined}$ ;
  for each  $a \in \mathcal{A}$  do  $D[a] := \text{False}$ ;  $T[a] := |\text{Tail}(a)|$ ;  $H[a] := |\text{Head}(a)|$  end_for;
   $Q^T := \{T\}$ ;  $Q^F := \{F\}$ ;
  repeat
    while  $Q^T \neq \emptyset$  do
      select and remove  $n \in Q^T$ ;
       $V[n] := \text{True}$ ;
      for each  $a \in \text{BS}(n)$  do  $D[a] := \text{True}$ ;
      for each  $a \in \text{FS}(n)$  such that  $D[a] = \text{False}$  do
         $T[a] := T[a] - 1$ ;
        if  $T[a] = 0$  and  $H[a] = 0$  then return (inconsistent);
        if  $T[a] = 0$  and  $H[a] = 1$ 
          then find  $n \in \text{Head}(a)$  such that  $V[n] = \text{undefined}$ ;  $Q^T := Q^T \cup \{n\}$  end_if;
        if  $T[a] = 1$  and  $H[a] = 0$ 
          then find  $n \in \text{Tail}(a)$  such that  $V[n] = \text{undefined}$ ;  $Q^F := Q^F \cup \{n\}$  end_if;
      end_for
    end_while;
    while  $Q^F \neq \emptyset$  do
      select and remove  $n \in Q^F$ ;
       $V[n] := \text{False}$ ;
      for each  $a \in \text{FS}(n)$  do  $D[a] := \text{True}$ ;
      for each  $a \in \text{BS}(n)$  such that  $D[a] = \text{False}$  do
         $H[a] := H[a] - 1$ ;
        if  $T[a] = 0$  and  $H[a] = 0$  then return (inconsistent);
        if  $T[a] = 0$  and  $H[a] = 1$ 
          then find  $n \in \text{Head}(a)$  such that  $V[n] = \text{undefined}$ ;  $Q^T := Q^T \cup \{n\}$  end_if;
        if  $T[a] = 1$  and  $H[a] = 0$ 
          then find  $n \in \text{Tail}(a)$  such that  $V[n] = \text{undefined}$ ;  $Q^F := Q^F \cup \{n\}$  end_if;
      end_for
    end_while;
  until  $Q^F = \emptyset$  and  $Q^T = \emptyset$ ;
end-procedure.

```

**4.2. Selection of a B-reduction**

The crucial part of the algorithm is the generation of the B-reduction and the search of a B-hyperpath in Step 3. A naïve approach could be to B-reduce each hyperarc  $a$ , choosing any node belonging to  $\text{Head}(a)$ , and then to apply procedure B-visit. However, it is not necessary to B-reduce all the hyperarcs: we are interested only in the B-hyperpaths contained in the B-reduction, and hence only hyperarcs which can belong to a B-hyperpath need to be B-reduced.

In practice, the generation of a B-reduction can be carried over incrementally while testing the existence of a B-hyperpath: a hyperarc  $a$  is B-reduced only if it is *connected*, i.e. if all the nodes in  $\text{Tail}(a)$  are B-connected to  $T$  in the partial B-reduction currently generated. At the beginning of the procedure, the connected hyperarcs are those whose tail contains the unique node  $T$ . It is easy to see that a node is B-connected to  $T$  if and only if it is chosen as the representative of the head of one of the connected hyperarcs. The incremental building of the B-reduction is performed until either a B-hyperpath from  $T$  to  $F$  is found (i.e., a B-hyperarc  $a$  such that  $\text{Head}(a) = \{F\}$  becomes connected) or no more connected hyperarcs exist. In the latter case, a feasible B-reduction for the hypergraph can be obtained by selecting any B-reduction for the hyperarcs that are not connected. The resulting procedure can be seen as a variant of procedure B-visit, in which only nodes that are selected are visited. Actually, the procedure finds a set of B-paths contained in the B-reduction; the list  $L$  contains the last hyperarc of each one of the detected paths. Note that each B-hyperarc of the type  $a = (\text{Tail}(a), \{F\})$  belongs at most to one B-hyperpath, that can easily be identified using a *predecessor function*  $\text{Pred}[n]$ ; for each node  $\text{Pred}[n]$  gives the hyperarc incident with  $n$  in a B-hyperpath from  $T$  to  $n$ . For each hyperarc  $a$ ,  $\text{BHead}[a]$  gives the node chosen to B-reduce  $a$ ; this node is returned by procedure  $\text{Choose\_head}(a)$ . The set  $Q$  contains the hyperarcs that are connected to  $T$  and must hence be B-reduced.

We give next a formal description of the procedure.

**Procedure B\_Reduction ( $\mathcal{H}$ : hypergraph)**

**begin**

**for each**  $n \in \mathcal{N}$  **do**  $\text{Pred}[n] := \text{null}$ ;

**for each**  $a \in \mathcal{A}$  **do**  $\text{BHead}[a] := \text{null}$ ;  $T[a] := |\text{Tail}(a)|$ ; **endfor**;

$L := \emptyset$ ;

$Q := \{a: a \in \text{FS}(T)\}$ ;

**while**  $Q \neq \emptyset$  **do**

**select and remove**  $a \in Q$ ;

$n := \text{Choose\_head}(a)$ ;

$\text{BHead}[a] := n$ ;

**if**  $\text{Pred}[n] = \text{null}$  **then**

**begin**

$\text{Pred}[n] := a$ ;

**for each**  $e \in \text{FS}(n)$  **such that**  $\text{BHead}[e] = \text{null}$

$T[e] := T[e] - 1$ ;

**if**  $T[e] = 0$

**then if**  $|\text{Head}(e)| = 0$  **then**  $L := L \cup \{e\}$

**else**  $Q := Q \cup \{e\}$ ;

**endif**

**endfor**

**end\_while**

**end-procedure**

It is easy to verify that the procedure correctly finds a B-hyperpath in the partial B-reduction generated, if one exists. To evaluate the complexity, notice that the inner “for\_each” loops are entered at most  $O(\mathcal{S}(\mathcal{H}))$  times, and that each arc is inserted and selected from  $Q$  only once.

Observe that a suitable policy for generating feasible B-reductions consists in keeping small the set of nodes B-connected to  $T$ . Hence, when B-reducing a hyperarc  $a$ , an already chosen node should be preferred, among the ones belonging to  $\text{Head}(a)$ ; although not described here, procedure Choose\_head exploits this idea. Since B-hyperarcs do not need to be B-reduced, the connected B-hyperarcs are the ones to be considered first; that helps in maintaining small the cardinality of the set of nodes B-connected to  $T$ . Following the same line of thought, we can conclude that connected hyperarcs whose heads contain fewer nodes should be considered first. Thus, a hyperarc with the head of minimum size is to be selected from  $Q$  at each step. Actually, we implement only partially this idea, since hyperarcs with more than two nodes in the head are selected with the same priority. This allows to perform the selection operation in time  $O(1)$ .

In practice, the procedure could be stopped when the first B-hyperpath from  $T$  to  $F$  has been found; the actual implementation behaves as follows: when a B-hyperpath is found, no more hyperarcs are inserted in  $Q$ , but the procedure continues until  $Q$  becomes empty. The branching set is obtained selecting from  $L$  a hyperarc  $a$  such that the number of nodes in  $\text{Tail}(a)$  is minimum.

Two versions of the algorithm have been implemented, which differ in the order in which the nodes are examined in the Choose\_head routine and in the branching operation. In the first version, BR1, the ordering of the nodes is arbitrary, while in the second, WBR, the nodes are assigned real weights and the nodes with higher values of the weights are examined first. The weights used are the ones proposed by Jeroslow and Wang [16], whose use has lead to an improved version of Davis and Putnam's algorithm. Such weights are defined for each node  $n$  by the formulas:

$$W^T(n) = \sum_{a \in \text{BS}(n)} 2^{-|a|},$$

$$W^F(n) = \sum_{a \in \text{FS}(n)} 2^{-|a|},$$

and can be considered as a sort of measure of the impact on the problem of fixing the variable  $n$  to *true* and to *false*, respectively. In fact,  $W^T(n)$  ( $W^F(n)$ ) increases with the number of clauses in whose consequence (implicant) the variable  $n$  appears, and decreases with the cardinality of the clauses. Actually, the larger is the number of clauses in which  $n$  appears in the consequence (implicant), the higher is the probability that variable  $n$  must be set to *true* (*false*) in a satisfying truth assignment, and the larger is the cardinality of a given clause, the less crucial is the rôle of  $n$  in making that clause satisfiable.

In WBR, the node chosen as the representative of the head in the B-reduction of a given hyperarc is the node with the minimum  $W^T(n)$  value among the ones in the

head, while in the branching operation the branching set is processed in decreasing order of  $W^F(n)$ , i.e. the node  $n$  with higher  $W^F(n)$  is fixed to *false* in the first subproblem generated  $P_1$ .

## 5. Computational results

The different versions of the algorithm have been tested on several classes of problems. In order to assess their performance, we have compared them to other existing algorithms, namely to two versions of the Davis and Putnam algorithm, and to the algorithm Horn2 proposed by Gallo and Urbani [13]. The reason of this choice is that such algorithms are considered to be quite efficient (see also Harche et al. [15]) and can be efficiently implemented making use of hypergraph data structure.

Davis and Putnam's algorithm, in the version on which our implementation is based, is an enumerative algorithm in which, when a subproblem  $P$  is examined, first unit resolution is applied in order to fix variables and then, if some clauses remain to be satisfied,  $P$  is split into two smaller subproblems  $P^T$  and  $P^F$  by selecting a proposition  $n$  and assigning it values *true* and *false*, respectively. The so called *pure literal rule* which is included in the original version of the algorithm (see [17]) has been dropped in our implementation, since it increases the computations without giving any real benefit. The crucial part of the method is the branching criterion, i.e. the selection of  $n$  and the order in which  $P^T$  and  $P^F$  are solved. In the simplest version, DPL/R, the selection of the branching proposition is made randomly, and the subproblem  $P^T$  is always solved first. In a second version, DPL/JW, the weights proposed in [16] are used, i.e. the node giving the maximum weight, either  $W^T(n)$  or  $W^F(n)$ , is selected and, respectively, problem  $P^T$  or  $P^F$  is solved first.

As for Horn2, our implementation differs from the one described in [13] in the fact that the Horn relaxation is solved by the procedure Unit-Resolution described in Section 4.

In order to obtain reasonably hard SAT instances it is necessary to select carefully the distribution model and the size of randomly generated problems. This is not a trivial task, since it is common experience that apparently reasonable distribution models might give quite poor results, that is instances that are very easy to solve (see, for example, [8, 9]). Usually, for a given distribution model, and a fixed number  $n$  of variables, the expected difficulty of the instances shows a typical dependence on the number of clauses  $m$ : in fact, it is usually possible to find values  $m_1$  and  $m_2 > m_1$  such that for  $m < m_1$  almost all the instances are satisfiable, while for  $m > m_2$  almost all the instances are unsatisfiable. The values  $m_1$  and  $m_2$  are usually quite close to each other, and they define the region in which the most difficult instances lay.

As an example, consider the distribution model used by Fedjki and Hooker [7] where the clauses are generated using the fixed probability model (see [9]), but eventually rejecting unit clauses in order to obtain harder problems. For 100 variables and an average number of literals per clause equal to 5, the most difficult problems are

the ones with  $m$  belonging to the interval  $[700, \dots, 1000]$ ; out of this range the problems become almost trivial, either because it is easy to find a satisfying truth assignment ( $m < 700$ ), or because inconsistencies arise after fixing a few variables ( $m > 1000$ ).

In addition to the randomly generated problems, we solved a set of *pigeon hole* problems (see [14]), and a set of unsatisfiable 3-SAT instances belonging to the class introduced by Urquhart [20]. The well-known pigeon hole problems consist in assigning  $n + 1$  “pigeons” to  $n$  “holes”, so that no two pigeons share the same hole. These problems are clearly unsatisfiable, and require exponential time to be solved by resolution, as proved by Haken [14]. Urquhart’s problems consist in assigning a 0–1 value to the edges of a graph, so that the sum modulo 2 of the values assigned to the arcs incident in each node is equal to the parity value assigned to the node. Urquhart [20] proved that some unsatisfiable problems in this class require exponential time for resolution.

The instances in these classes show a very particular structure (e.g., a pigeon-hole problem contains only positive clauses and negative 2-clauses) and are usually hard to solve also for enumerative methods, becoming intractable for relatively small input sizes.

The results of our computational experiences are listed below. For each distribution and problem size (except for two cases in Table 1) ten instances were solved. We report the average number of nodes in the enumeration tree and the CPU time used for solving the problem (excluding I/O and system time); times are seconds on a HP 9000/720. The algorithms were implemented in standard C language. It is worth noting that the algorithms share the same code for the common parts, and substantially differ only in the implementation of the branching step.

Table 1 contains the results for the problems used in [7]; BR1 and WBR perform better than the other algorithms, and WBR explores fewer nodes than BR1. For these problems, algorithm DPL/JW performs better than Horn2, except in one case (problems with 700 clauses); Horn2 performs better than DPL/R.

In Table 2 we report the results for 3-SAT problems, that substantially confirm the ones of the previous class. Note however that in one case BR1 enumerates more nodes than DPL/JW. For these instances WBR always dominates BR1; DPL/JW always outperforms Horn2, and the latter outperforms DPL/R.

Problems in Table 3 are generated with a discrete uniform model: the number  $d$  of literals per clause ranges between 3 and 7 with equal probability. This distribution was chosen in order to obtain the same average  $d = 5$  of the problems in Table 1; however the resulting instances are much more difficult. We previously found that a uniform distribution in  $[2, \dots, 8]$  results in very easy problems, probably because many clauses of length 2 are generated.

For these instances BR1 enumerates more nodes than DPL/JW (except for two cases); however, the former takes less CPU time than the latter for instances with more than 1200 clauses, that are the hardest to solve for DPL/JW. Both DPL/JW and BR1 are outperformed by WBR. Also in this case, DPL/JW always outperforms Horn2, and Horn2 outperforms DPL/R.

Table 1  
Fixed probability model ( $n = 100$ )

% of inst.	$m$	% sat. inst.	Average CPU times (s)				
			DPL/JW	DPL/R	Horn2	BR1	WBR
10	700	100	0.32	1.14	0.17	0.15	0.08
20	800	55	0.49	3.98	1.67	0.25	0.17
20	850	20	0.91	5.30	1.83	0.34	0.34
10	900	10	0.64	4.12	1.37	0.24	0.19
10	1000	0	0.22	0.47	0.32	0.10	0.08
Total			2.58	15.01	5.36	1.08	0.86
Average # of nodes enumerated in the search tree							
10	700	100	131	1104	159	70	30
20	800	55	135	2981	1145	89	50
20	850	20	238	3592	1085	117	96
10	900	10	147	2678	680	86	53
10	1000	0	34	214	117	21	13
Total			685	10569	3186	383	242

Table 2  
3-SAT problems ( $n = 100$ )

$m$	% sat. instances	Average CPU times (s) – 10 instances				
		DPL/JW	DPL/R	Horn2	BR1	WBR
350	100	0.06	0.34	0.23	0.05	0.04
400	80	1.36	26.65	5.10	0.62	0.63
420	50	2.12	18.40	5.92	1.30	0.79
450	10	1.85	17.37	4.44	1.56	1.00
500	0	1.54	8.98	1.96	1.01	0.62
Total		6.93	71.74	17.65	4.54	3.08
Average # of nodes enumerated in the search tree – 10 instances						
350	100	37	517	381	42	21
400	80	859	34455	6303	489	405
420	50	1264	22529	6672	976	496
450	10	991	19578	4603	1073	581
500	0	730	9279	1833	647	329
Total		3881	86358	19792	3227	1832

From the results of Table 3, one may conjecture that the search strategy based on B-reductions gives worse results, when the number of literals per clause varies in a short interval, with a relatively high average value. This hypothesis is confirmed by the results of 5-SAT problems, listed in Table 4. Note that, in order to solve the



Table 3  
Discrete uniform distribution in  $[3..7]$  ( $n = 100$ )

n	% of sat. instance	Average CPU time (s) – 10 instances				
		DPL/JW	DPL/R	Horn2	BR1	WBR
1000	100	0.36	19.99	1.79	0.51	0.17
1100	100	8.86	267.08	24.95	20.29	2.90
1200	80	60.96	2 607.01	213.86	124.47	47.33
1250	40	177.85	2 159.21	273.80	113.84	57.14
1300	20	167.88	2 352.30	233.34	130.72	59.24
1400	10	128.63	1 770.25	242.49	94.90	53.62
1500	0	90.59	773.69	117.98	47.75	28.68
	Total	635.13	9 949.53	1 108.21	532.48	249.08
Average # of nodes enumerated in the search tree – 10 instances						
1000	100	80	12 894	1 069	176	28
1100	100	1 937	155 053	12 504	6 098	652
1200	80	10 995	1 264 782	88 602	32 924	8 497
1250	40	29 851	1 182 999	104 485	27 709	9 547
1300	20	24 751	952 728	81 201	29 057	9 067
1400	10	15 179	597 163	79 732	17 450	7 116
1500	0	8 941	220 736	28 326	7 680	3 494
	Total	91 734	4 386 355	395 919	121 094	38 401

Table 4  
5-SAT problems ( $n = 50$ )

m	% of sat. instance	Average CPU time (s) – instances				
		DPL/JW	DPL/R	Horn2	BR1	WBR
900	100	1.03	38.60	14.93	13.73	4.96
1000	80	99.14	473.39	130.86	129.40	98.59
1100	10	306.71	951.24	284.86	321.55	195.73
1200	10	261.90	816.57	213.99	270.92	176.58
1300	0	251.36	692.09	201.29	225.06	156.17
	Total	920.14	2 971.89	845.93	960.66	632.03
Average # of nodes enumerated in the search tree – 10 instances						
900	100	371	30 051	8 854	5 636	1 618
1000	80	26 880	314 709	65 336	45 407	27 308
1100	10	69 604	552 354	123 948	96 989	46 840
1200	10	51 195	416 655	80 373	69 302	35 650
1300	0	39 889	298 044	63 864	48 723	26 714
	Total	187 939	1 611 813	342 375	266 057	138 130

problems in a reasonable amount of time, we had to generate problems with  $n = 50$  variables.

Actually, DPL/JW enumerates less nodes than BR1, and, in two cases, also less nodes than WBR. However, WBR is outperformed by DPL/JW only for the easy

Table 5  
Pigeon-hole problems

# of pigeons	<i>n</i>	<i>m</i>	CPU times (s)				
			DPL/JW	DPL/R	Horn2	BR1	WBR
6	30	81	0.17	0.10	0.07	0.10	0.10
7	42	133	1.68	0.81	0.40	0.70	0.77
8	56	204	19.17	7.45	3.08	6.05	7.07
9	72	297	243.43	82.88	27.70	58.98	65.00
10	90	415	3489.73	988.57	277.38	647.27	727.78
# of nodes generated in the search tree							
6	30	81	749	423	206	263	263
7	42	133	6491	3081	1237	1663	1663
8	56	204	65561	23313	8660	11523	11523
9	72	297	756687	225129	69281	92991	92991
10	90	415	9825029	2259295	623530	859911	859911

Table 6  
Urquhart's 3-SAT problems

Graph nodes	<i>n</i>	<i>m</i>	Average CPU time (s) – (5 instances)				
			DPL/JW	DPL/R	Horn2	BR1	WBR
10	15	40	0.03	0.02	0.02	0.02	0.03
20	30	80	1.46	0.76	0.62	0.55	0.68
30	45	120	54.44	25.16	16.76	11.07	14.38
40	60	160	2176.73	795.18	425.66	182.33	227.87
Average # of nodes generated in the search tree (5 instances)							
10	15	40	127	127	121	119	119
20	30	80	4095	4543	3846	2582	2582
30	45	120	131071	148019	104644	50687	50687
40	60	160	4194303	4510994	2637355	809499	809499

problems with 900 clauses. Note that for this class both BR1 and DPL/JW are outperformed by Horn2, even if the latter enumerates more nodes.

Table 5 contains the results for the pigeon-hole problems. The results for Urquhart's problems are listed in Table 6: we generated instances corresponding to 4 graphs, with 10 up to 40 nodes; for each graph, we generated 5 instances, assigning different parity values to the nodes.

Note that, due to the particular structure of these problems, some algorithms show a "pathological" behaviour: in fact, DPL/JW always gives the worst results, and is outperformed even by DPL/R. This kind of behaviour was observed also in [15], for several particular classes of problems. Furthermore, DPL/JW enumerates  $2^{(n/3)+2} - 1$  nodes when applied to a Urquhart's problem with *n* variables. On the contrary, Horn2

Table 7  
Average execution time (sat. and unsat. instances)

Model		% of sat. instances	DPL/JW	DPL/R	Horn2	BR1	WBR	
Fixed								
Prob	$n = 100$	55	0.38	4.24	1.69	0.21	0.15	sat
	$m = 800$		0.64	3.68	1.64	0.30	0.20	unsat
3-SAT	$n = 100$	50	0.80	9.64	2.82	0.37	0.27	sat
	$m = 420$		3.45	27.17	9.02	2.22	1.32	unsat
[3..7] SAT	$n = 100$	40	135.86	2159.21	141.90	47.38	32.65	sat
	$m = 1250$		205.84	3004.21	361.74	158.15	73.47	unsat
5-SAT	$n = 50$	80	33.07	241.06	77.93	59.11	57.15	sat
	$m = 1000$		363.42	1402.70	342.58	410.57	264.31	unsat

is much more stable, and gives the best results for pigeon-hole problems. It is worth noticing that for these problems algorithms BR1 and WBR enumerate the same number of nodes.

In order to give an explanation of the experimental results, first note that the B-reduction method, as implemented in BR1 and WBR, can be seen as an extension of algorithm Horn2. In fact, the two methods share an equivalent branching rule, which is, respectively, applied to hyperarcs incident with node  $F$  and with node  $T$ . In addition, algorithms BR1 and WBR incorporate a heuristic for finding solutions (Step 3) and a selection rule for the branching step. The underlying idea of the selection rule is to concentrate on those situations in which a potential contradiction, i.e. a B-path, has been detected. This contradiction may be ruled out by branching, or may lead to an early detection of the unsatisfiability of the subproblem.

The selection rule based on the search of a B-reduction proves to be almost as effective as the one based on Jeroslow and Wang's weights; in fact, even the simpler implementation of our method, BR1, compares favourably with algorithm DPL/JW. Actually, BR1 is slightly worse than DPL/JW only for 5-SAT problems; for this class the selection rules turn out to be too expensive, and both BR1 and DPL/JW are outperformed by Horn2. As one might expect, if the search of B-reductions takes advantage of weights a much faster algorithm is obtained. Note however that for the problems in Tables 5 and 6 the weights do not provide useful informations; nevertheless, the use of B-reductions is still useful, at least for Urquhart's problems.

Computational results show that the B-reduction method is not always the best for easy satisfiable instances. In Tables 7 and 8 we investigate the relationship between the behaviour of the algorithms and the satisfiability of the problems. For each one of the classes in Tables 1–4 we choose the group of problems which are satisfiable in about half of the cases. In order to allow for meaningful comparison, in Table 7 we report separately the average execution times of the algorithms on the satisfiable instances and on the unsatisfiable ones. In Table 8, for each class, we report the above times

Table 8  
Normalized average execution time (sat. and unsat. instances)

Model		% of sat instances	DPL/JW	DPL/R	Horn2	BR1	WBR	
Fixed Prob	$n = 100$	55	7.60	10.64	10.12	8.30	8.66	sat
	$m = 800$		12.92	9.22	9.86	12.08	11.84	unsat
3-SAT	$n = 100$	50	3.77	5.24	4.77	2.88	3.36	sat
	$m = 420$		16.23	14.76	15.23	17.12	16.64	unsat
[3..7] SAT	$n = 100$	40	7.64	8.10	5.18	4.16	5.71	sat
	$m = 1250$		11.57	11.27	13.21	13.89	12.86	unsat
5-SAT	$n = 50$	80	3.34	5.09	5.96	4.57	5.80	sat
	$m = 1000$		36.66	29.63	26.18	31.73	26.81	unsat

normalized with respect to the total execution time for the problems in that class. The expected normalized value is 10; a large difference between the two values corresponding to the same group reveals a strong dependence on the satisfiability of the problem.

The results in Table 7 substantially confirm the previous observations, and do not show any pathological dependence. From Table 8 it turns out that WBR is more stable than BR1, i.e. it shows a weaker dependence on the satisfiability of the problems: this can be explained by the more effective selection rule. Note also that DPL/R and Horn2, which do not incorporate any selecting or guessing heuristic, are usually stable.

## 6. Conclusions and further work

In this paper the satisfiability problem is stated in terms of directed hypergraphs. The resulting formulation is then reduced to the problem of finding a particular B-reduction. An enumerative algorithm that follows this approach is proposed, and two different implementations are tested. It seems apparent from the computational results that the proposed approach is effective on a wide range of problems. It would be interesting to verify its behaviour on practical problems, such as instances arising from large non-Horn propositional databases.

It should be observed that the present versions of the algorithm are based on a relatively straightforward application of results arising from the theory of hypergraphs. Further developments are possible; in particular, a deeper investigation of the structure of the hypergraph model may suggest algorithmic improvements, while the use of reoptimization techniques may lead to faster implementations. An interesting open problem, which we would like to address in the future, is the one of choosing the most effective branching set among the ones existing in a given B-hyperpath.

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